

AMENDMENTS TO THE CLAIMS

1. (Currently Amended) A digital video communication system comprising:  
at a source site[[,]]:  
an embedding circuit for embedding a digital watermark in a digital video stream to produce a watermarked digital video stream; and  
a multiplexer for multiplexing a text data stream with the watermarked digital video stream to produce a multiplexed signal,  
at a sink site[[,]]:  
a demultiplexer for demultiplexing the multiplexed signal for recovering said watermarked digital video stream and said text data stream;  
a digital watermark detector for detecting the digital watermark embedded in the recovered digital video stream; and  
a ~~synchroniser~~ synchronizer for ~~synchronising~~ synchronizing the recovered text data stream to the recovered digital video stream in response to the detection of said digital watermark.
2. (Currently Amended) The digital video communication system of claim 1, further comprising a digital overlay circuit for superimposing the ~~synchronised~~ synchronized text data stream with the recovered digital video stream.
3. (Currently Amended) The digital video communication system of claim 1, wherein said ~~synchroniser~~ synchronizer comprises a memory.

4. (Currently Amended) The digital video communication system of claim 1, wherein said ~~synchroniser~~ synchronizer comprises a dual-mode memory.

5. (Currently Amended) The digital video communication system of claim 1, wherein said ~~synchroniser~~ synchronizer comprises a pair of first and second memories and a control circuit for alternately operating the first and second memories in write and read modes in response to the detected digital watermark.

6. (Original) The digital video communication system of claim 5, further comprising a digital overlay circuit for superimposing the text data stream read out of each of said first and second memories with the recovered digital video stream.

7. (Original) The digital video communication system of claim 1, further comprising at said sink site:  
a copy protect circuit responsive to the detected digital watermark for producing a copy management signal; and  
an embedding circuit for embedding the copy management signal in the recovered digital video stream as a second digital watermark for preventing illegal duplication of the digital video stream.

8. (Original) The digital video communication system of claim 1, further comprising at said sink site:  
means for converting the detected digital watermark to a second digital watermark; and  
an embedding circuit for embedding the second digital watermark in the recovered digital video stream.

9. (Original) The digital video communication system of claim 1, further comprising at said source site:

a video compression circuit for compressing the watermarked digital video stream to supply an MPEG-2 transport stream to said multiplexer;

an encryption circuit for encrypting the multiplexed signal; and

a forward error correction (FEC) encoder for encoding the encrypted signal,

at said sink site,

an FEC decoder for decoding the encoded signal to recover an encrypted signal;

a decryption circuit for decrypting the encrypted signal to recover a multiplexed signal of said MPEG-2 transport stream and said text data stream and supplying the multiplexed signal to said demultiplexer, whereby the MPEG-2 transport stream and said text data stream are individually recovered by said demultiplexer, and

a video expansion circuit for expanding the MPEG-2 transport stream recovered by said demultiplexer to supply a signal corresponding to said watermarked digital video stream to said watermark detector.

10. (Currently Amended) A digital video communication method comprising the steps of:

a) embedding a digital watermark in a digital video stream to produce a watermarked digital video stream;

b) multiplexing a text data stream with the watermarked digital video stream to produce a multiplexed signal;

- c) demultiplexing the multiplexed signal for recovering a watermarked digital video stream and a text data stream;
- d) detecting the digital watermark embedded in the recovered digital video stream and using the detected digital watermark as a timing signal for reading the text data from said storage medium; and
- e) ~~synchronising~~ synchronizing the recovered text data stream with the recovered digital video stream in response to the detected digital watermark.

11. (Original) The method of claim 10, wherein the step (e) comprises storing the recovered text data stream in a memory and reading the stored text data stream from the memory in response to the detected digital watermark.

12. (Currently Amended) A digital television receiver for receiving a digital video signal in which a digital video stream and a text data stream are multiplexed and the digital video stream is watermarked by a digital watermark signal associated with the text data stream, comprising:

- a demultiplexer for demultiplexing the digital video signal for recovering the watermarked digital video stream and the text data stream;
- a digital watermark detector for detecting the digital watermark in the recovered digital video stream; and
- a ~~synchroniser~~ synchronizer for ~~synchronising~~ synchronizing the recovered text data stream to the recovered digital video stream in response to the detection of said digital watermark.

13. (Currently Amended) The digital television receiver of claim 12, further comprising a digital overlay circuit for superimposing the ~~synchronised~~ synchronized text data stream with the recovered digital video stream.

14. (Currently Amended) The digital television receiver of claim 12, wherein said ~~synchroniser~~ synchronizer comprises a memory.

15. (Currently Amended) The digital television receiver of claim 12, wherein said ~~synchroniser~~ synchronizer comprises a dual-mode memory.

16. (Currently Amended) The digital television receiver of claim 12, wherein said ~~synchroniser~~ synchronizer comprises a pair of first and second memories and a control circuit for alternately operating the first and second memories in write and read modes in response to the detected digital watermark.

17. (Original) The digital television receiver of claim 16, further comprising a digital overlay circuit for superimposing the text data stream read out of each of said first and second memories with the recovered digital video stream.

18. (Original) The digital television receiver of claim 12, further comprising:

a copy protect circuit responsive to the detected digital watermark for producing a copy management signal; and

an embedding circuit for embedding the copy management signal in the recovered digital video stream as a second digital watermark for preventing illegal duplication of the digital video stream.

19. (Original) The digital video communication system of claim 12, further comprising:
- means for converting the detected digital watermark to a second digital watermark; and
  - an embedding circuit for embedding the second digital watermark in the recovered digital video stream.